FIG 1

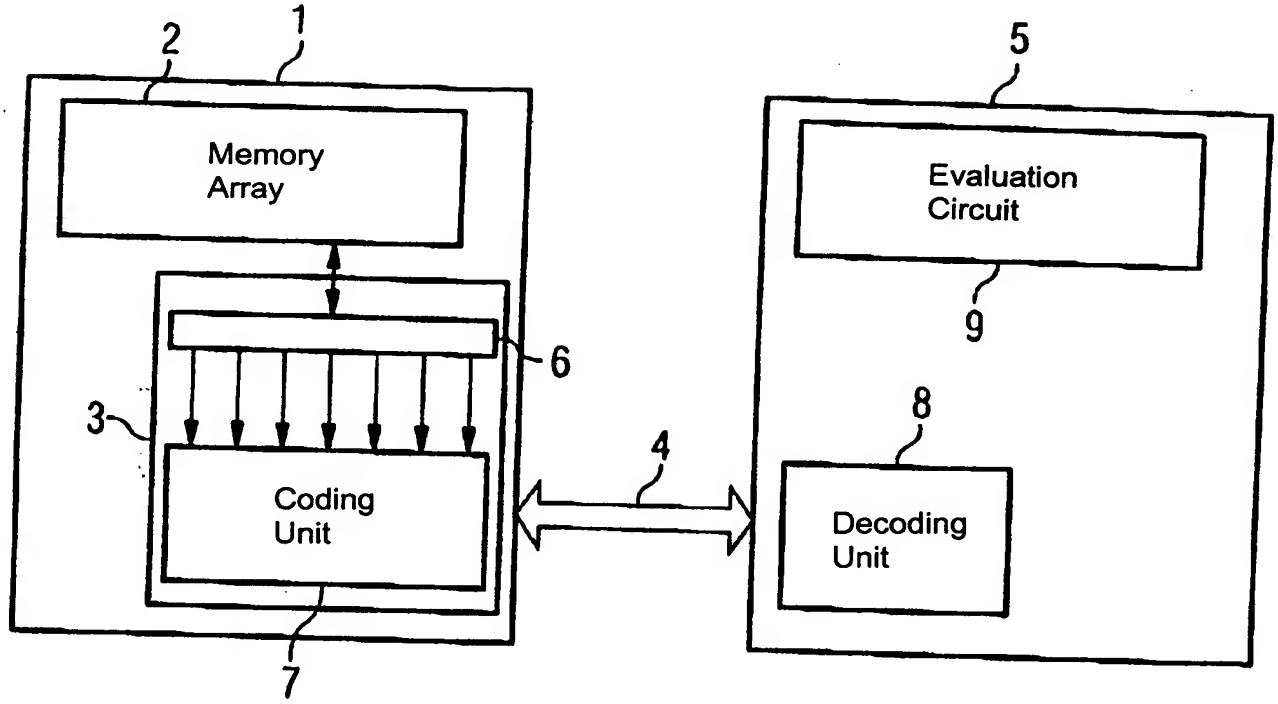
1
2
5
Memory Array
Evaluation Circuit
9
6
3
8
4
Coding Unit
Decoding Unit
7

FIG 2

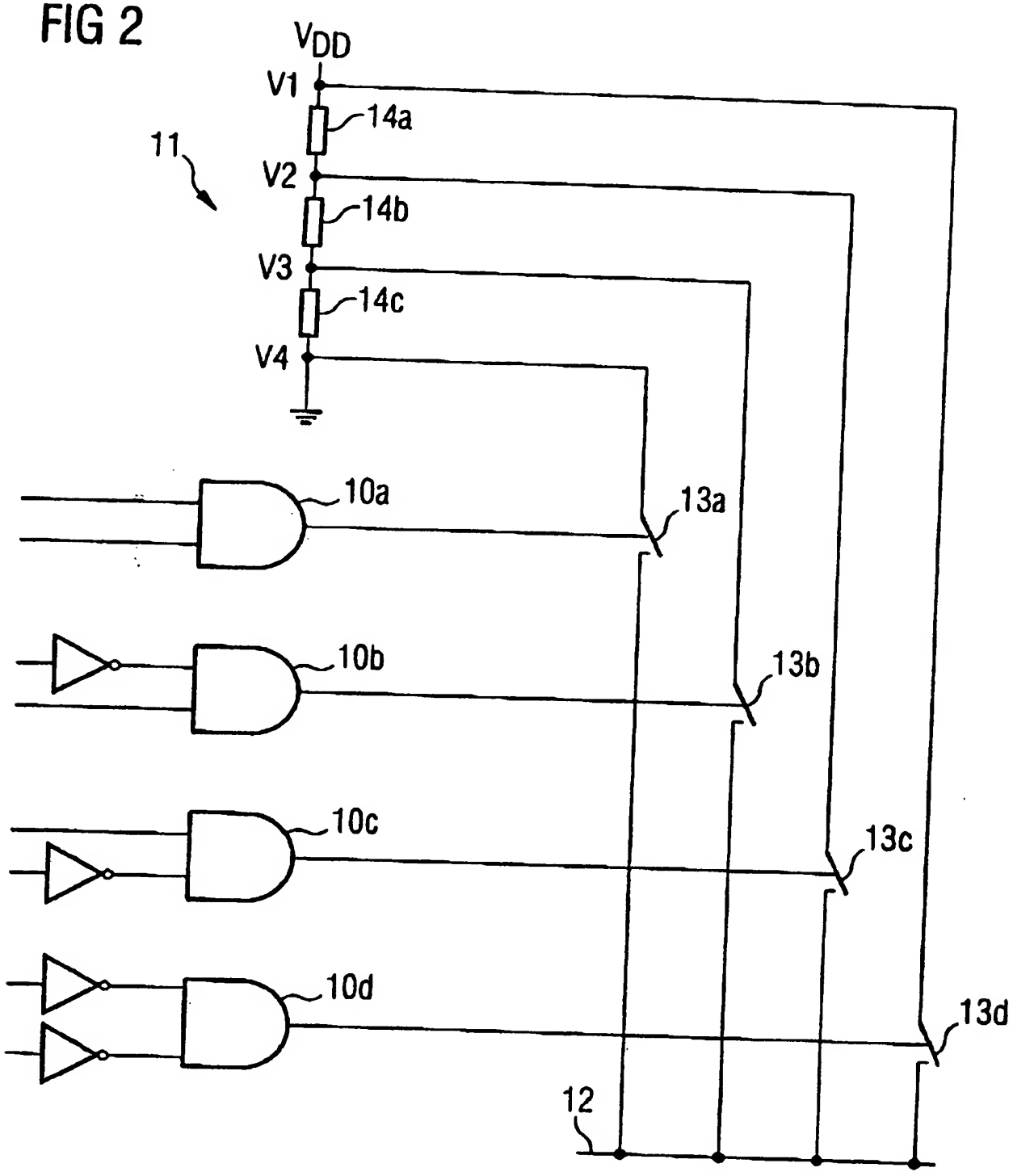

FIG 3

Bit 1	Bit 2	V_out
HIGH	HIGH	V_4
LOW	HIGH	V_3
HIGH	LOW	V_2
LOW	LOW	V_1
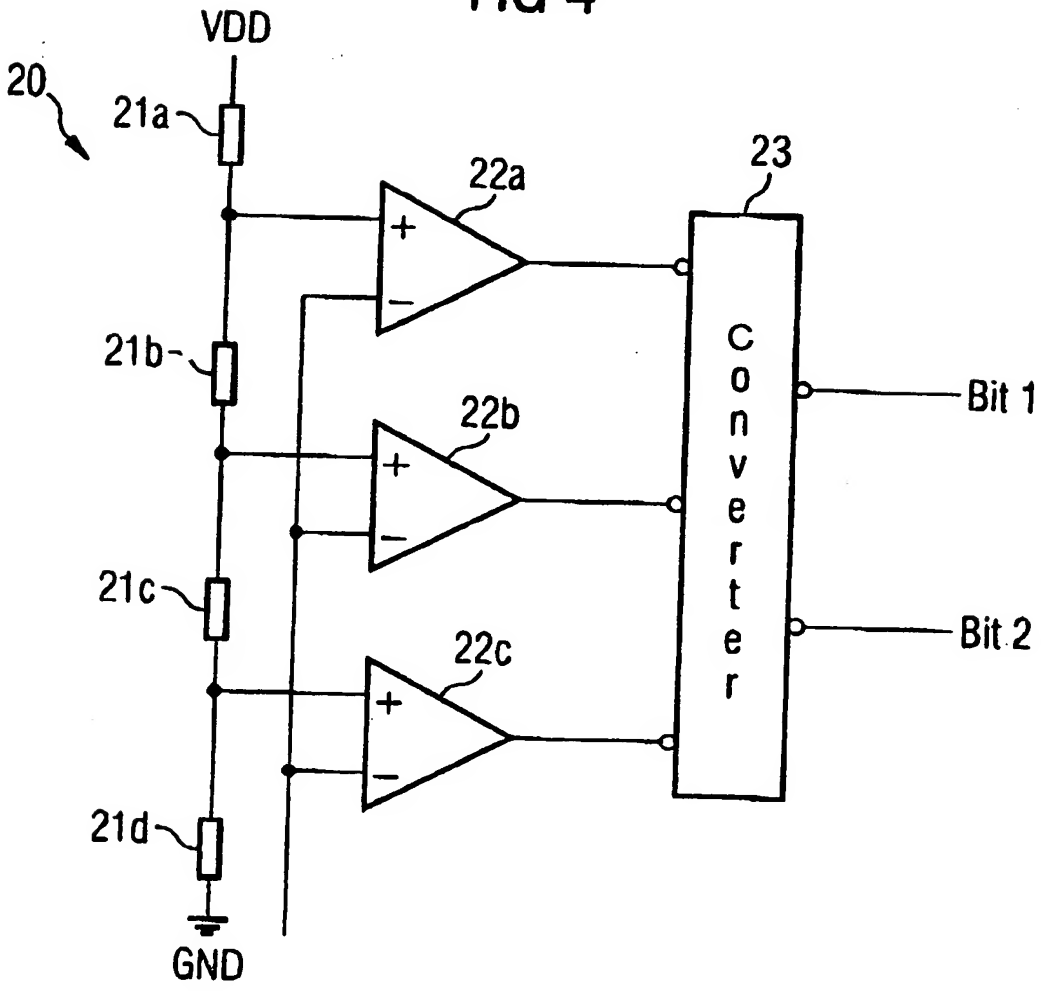
FIG 4